

ETC

Notice of Allowability

Application No.

10/633,074

Examiner

Tod T. Van Roy

Applicant(s)

SELF ET AL.

Art Unit

2828

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08/19/2005.
2. ☒ The allowed claim(s) is/are 1-3,10-15,18-21.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

Response to Amendment

The examiner acknowledges the cancellation of claims 4-9, 16-17, and 22, as well as the amending of claims 1 and 19.

The examiner also acknowledges the changes made to the applicant's specification.

Allowable Subject Matter

Claims 1-3, 10-15, and 18-21 are allowed.

Claim 1 is believed to be allowable based on the fact that a driving system for an optoelectronic device with a buffer circuit receiving a differential input signal, a voltage regulator providing an output voltage as a function of the control signal, a driver amplifier comprising: first and second differentially connected transistors and a first current source, third and fourth differentially connected transistors and a second current source, a time delay network connected between the inputs of the transistor pairs; the driver amplifier being coupled to the voltage regulator and providing a precise waveform to the optoelectronic device, and having a first input coupled to the base of the first and third transistors, and a second input coupled to the base of the second and fourth transistors, was not found to be taught in the prior art.

Claims 2-3, 10-15, and 18, are believed to be allowable as they depend from allowable claim 1.

Art Unit: 2828

Claim 19 is believed to be allowable based on the fact that a driving system for an optoelectronic device with a buffer circuit receiving a differential input signal, a voltage regulator providing an output voltage as a function of the control signal, a driver amplifier comprising: first and second amplifying transistors adapted to receive a differential input to their base regions, the emitters connected to form a node, first and second load resistors connected between the collectors of the transistors and the voltage regulator, an output to the optoelectronic device connected between the collector of the second transistor and the second load resistor, a third transistor connected between the common node and ground, the voltage regulator having an output transistor, a fourth transistor connected to the output transistor forming a current mirror, and a fifth transistor connected to the third transistor forming an additional current mirror; the driver amplifier having a first input coupled to the output of the buffer circuit and additionally coupled to the voltage regulator providing a precise waveform to the optoelectronic device, was not found to be taught in the prior art.

Claims 20-21 are believed to be allowable as they depend from allowable claim 19.

Conclusion

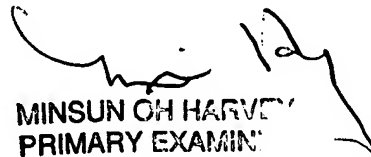
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tod T. Van Roy whose telephone number is (571)272-8447. The examiner can normally be reached on M-F.

Art Unit: 2828

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Minsun Harvey can be reached on (571)272-1835. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TVR


MINSUN OH HARVEY
PRIMARY EXAMINER